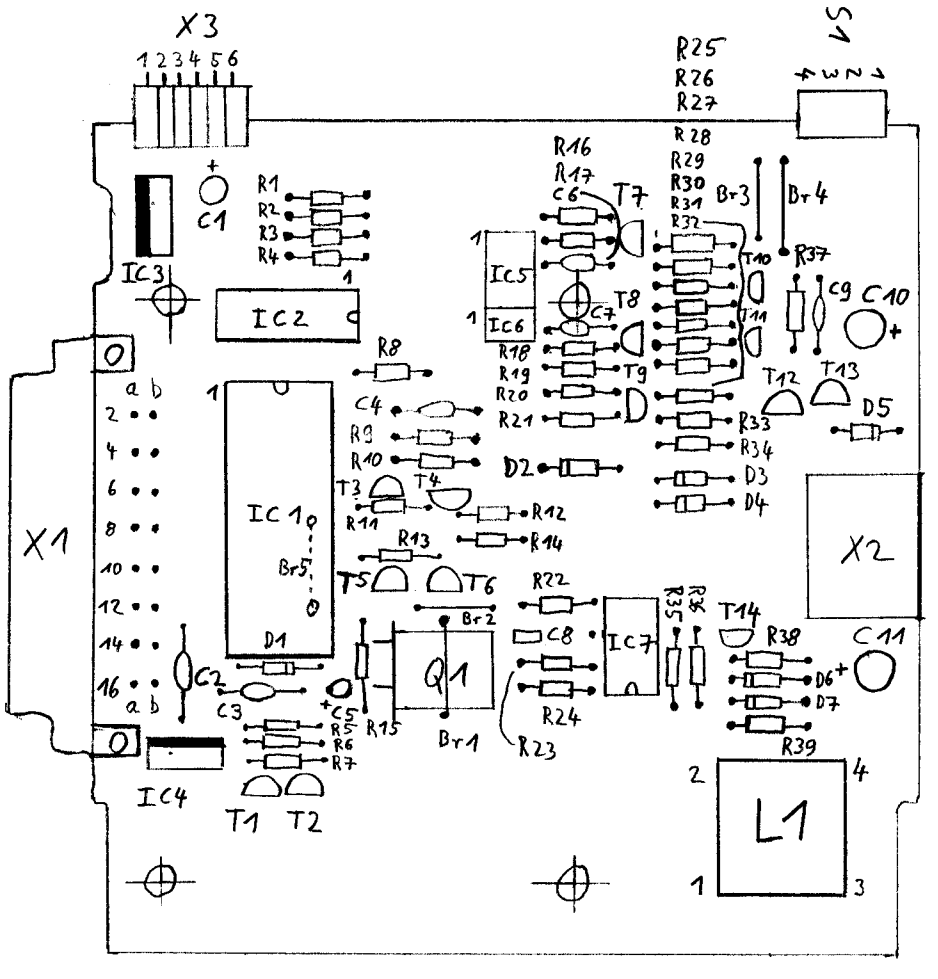
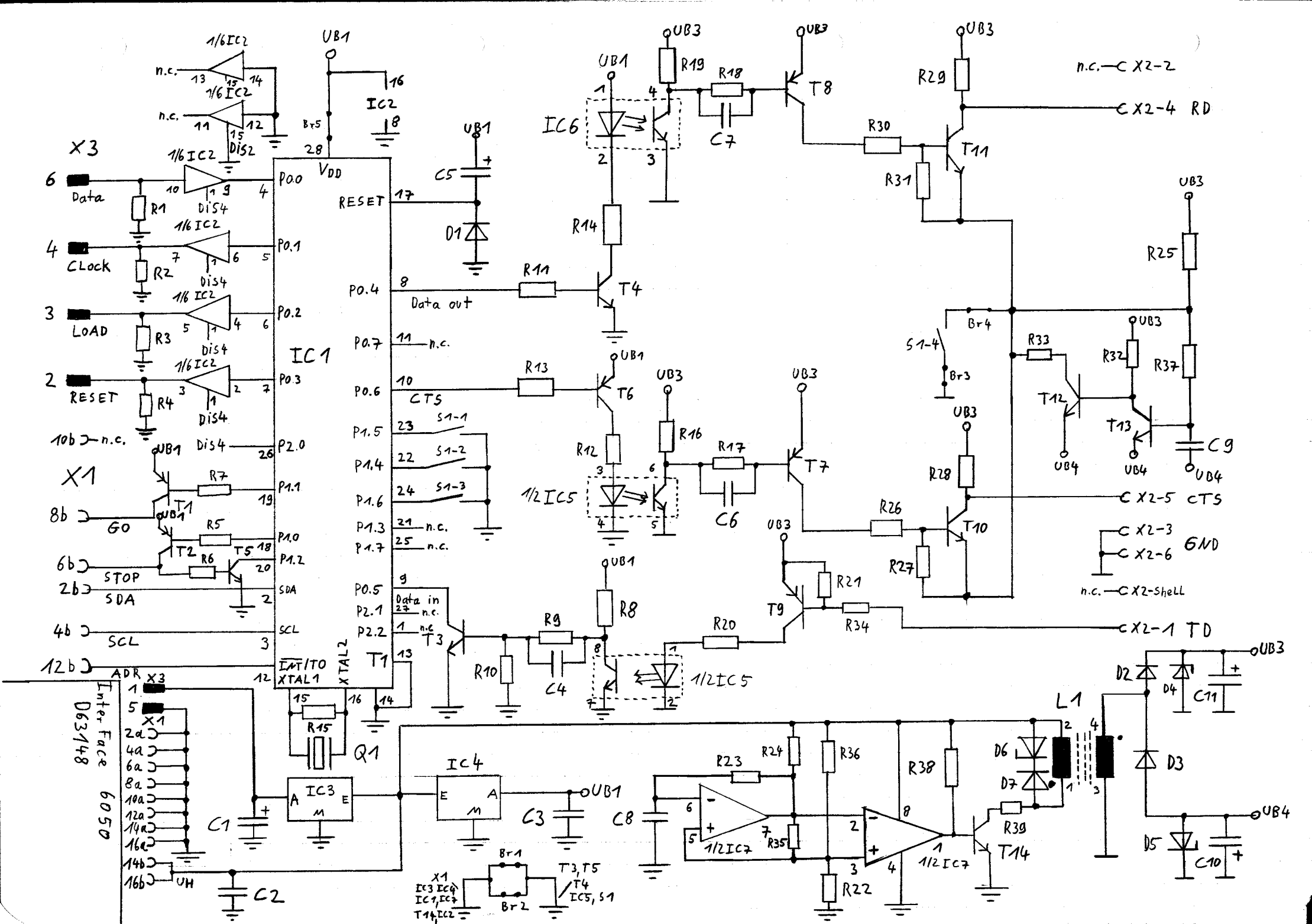

X3
1 2 3 4 5 6
S1
4 3 2 1
R25
R26
R27
R28
R29
R30
R31
R32
R16
R17
C6
T7
Br3
Br4
T10
T11
R37
C9
C10
R1
R2
R3
R4
C1
IC3
IC2
IC5
IC6
T8
C7
R18
R19
R20
R21
T9
R8
C4
R9
R10
T3
T4
R11
R12
R13
R14
T5
T6
D2
T12
T13
D5
R33
R34
D3
D4
X1
X2
IC1
Br5
D1
C2
C3
C5
R5
R6
R7
IC4
T1
T2
Br2
Q1
Br1
R15
R23
R22
C8
IC7
R35
R36
R24
T14
R38
C11
D6
D7
R39
L1
2
4
1
3


IC1
X3
X1
X2
IC2
IC3
IC4
IC5
IC6
IC7
T1
T2
T3
T4
T5
T6
T7
T8
T9
T10
T11
T12
T13
T14
Q1
L1
D1
D2
D3
D4
D5
D6
D7
R1
R2
R3
R4
R5
R6
R7
R8
R9
R10
R11
R12
R13
R14
R15
R16
R17
R18
R19
R20
R21
R22
R23
R24
R25
R26
R27
R28
R29
R30
R31
R32
R33
R34
R35
R36
R37
R38
R39
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
VDD
RESET
Data out
CT5
Data in
SDA
SCL
INT/TO
XTAL1
XTAL2
ADR
Br1
Br2
Br3
Br4
S1-1
S1-2
S1-3
S1-4
Data
CLock
LOAD
RESET
GO
STOP
SDA
SCL
ADR
UH
n.c.
GND
TD
RD
CT5
UB1
UB3
UB4
Inter Face 6050
D6348

All resistors 1/3 W carbon, ±5%

R1 ... R5, R7, R13 : 47 KΩ

R6, R15 : 1 MΩ

R8, R12, R14, R16, R19, R20, R28, R29 : 1,2 KΩ

R9, R17, R18, R21, R25, R37 : 33 KΩ

R10, R11, R26, R30, R32, R24 : 10 KΩ

R22, R34, R35, R36 : 100 KΩ

R23 : 270 KΩ

R27, R31, R38 : 4,7 KΩ

R33 : 27 Ω

R39 : 22 Ω

C1 : 10 µF / 35 V Tantalum

C2, C3, C4, C6, C7, C9 : 100 nF Ceramic (104 A7M)

C5 : 1 µF / 35 V Tantalum

C8 : 150 pF Ceramic (body: grey top: pink)

C10, C11 : 100 µF / 25 V Electrolytic cap.

D1, D2, D3, D7 : 1N 4148 (Philips)

D4, D5, D6 : ZPD 5.6 (ITT)

T1, T2, T7, T8, T9, T6 : BC 557 B (pnp)

T3, T4, T5, T10, T11, T12, T13 : BC 547 B (npn)

T14 : BC 337 B (539)

IC1 : MAB 8420 P C052 (DSD 531 V8Y Philips)

IC2 : CD 4503 BCN MM 80C97N (P 8432 National)

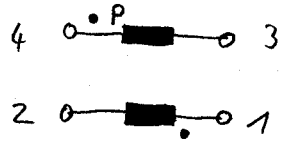
IC3, IC4 78005 AP (Toshiba 5D) (Toshiba 5C)

IC5 PC 827 (PC817 T2 SHARP)

IC6 PC 817 (PC817 T7 SHARP)

IC7 TDB 0193 DP (338)

L1 IL1-11-1 (timonta)



Q1 4,000 MHz HC18/U

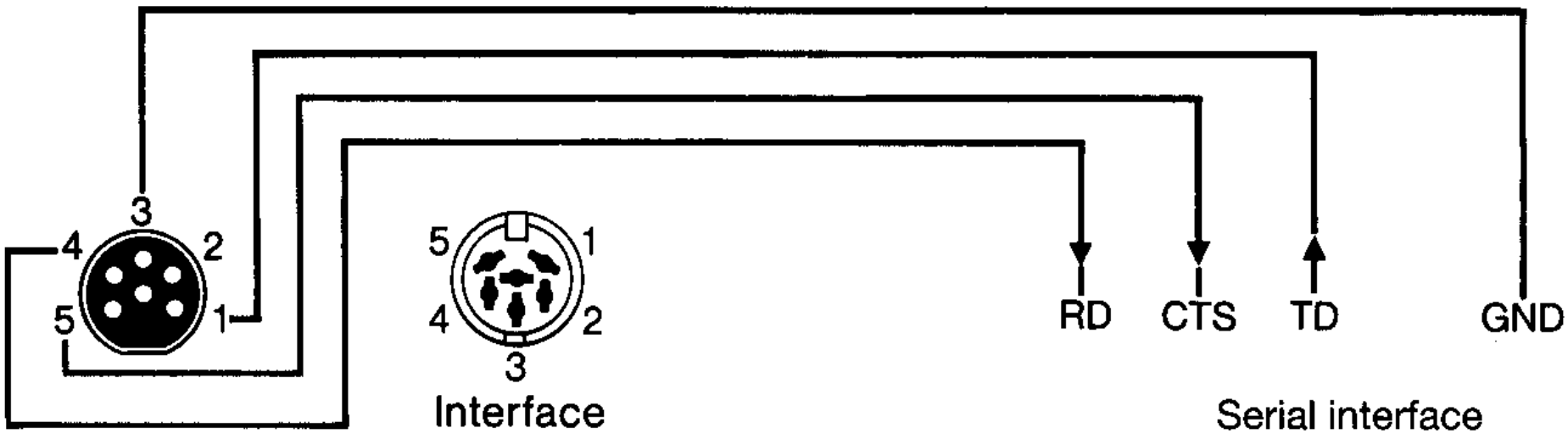
X1 Edge connector DIN 41612, B/2, female, only even pins inserted

X2 Hirschmann

X3 Panduit 6 way

S1 Dip-switch, quad (C&K)

Br1 ... Br5 tinned wire dia = 0,6 mm



Contact no.	Designation	Function	
1	TD	TRANSMIT DATA	– Data line from computer to Interface
2	--	not used	
3	GND	GROUND	
4	RD	RECEIVE DATA	– Data line from Interface to computer
5	CTS	CLEAR TO SEND	– Control line for Interface readiness to receive data

Switch	for	Setting on	Setting off
1	TD	negative logic	positive logic
2	RD	negative logic	positive logic
3	CTS	negative logic	positive logic
4	GND	TTL level	RS 232 level (+/– 5 volts)

The correct setting for a computer with a standard RS 232 Interface is:

Switch	Setting
1	on
2	on
3	off
4	off